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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/618,873	07/14/2003	Jerome Azema	TI-34922	8044

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EXAMINER	
GERGISO, TECHANE	
ART UNIT	PAPER NUMBER
2137	

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	01/12/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)	
	10/618,873	AZEMA ET AL.	
	Examiner	Art Unit	
	Techane J. Gergiso T.G	2137	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *14 July 2003*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-22* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-22* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☒ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date *12/01/2003*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

1. This is a non-Final Office Action in response to the application filed on July 14, 2003.

2. Claims 1-22 have been examined.

3. Claims 1-22 are pending.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 1-22 are rejected under 35 U.S.C. 102(e) as being anticipated by Geiger et al. (hereinafter referred to as Geiger, US Pat No.: 6, 463, 534).

As per claim 1:

Geiger discloses a method of configuring a processing device, comprising the steps of:

accessing a certificate bound to the processing device (column 3: lines 14-21; column 4: lines 23-35, lines 59-67; column 18: lines 45-60; column 19: lines 9-28);

authenticating the certificate (column 11: lines 46-54; column 16: lines 10-43;column 18: lines 1-5; column 18: lines 37-45);

reading configuration parameters from the certificate, if properly authenticated; configuring the processing device responsive to the configuration parameters (column 4: lines 23-35).

As per claims 2 and 13:

Geiger discloses a method, wherein the steps of accessing the certificate, authenticating the certificate, and reading configuration parameters from the certificate are performed whenever the processing device is initially powered (figure 2: 130; column 6: lines 5-45).

As per claim 3:

Geiger discloses a method, wherein the steps of accessing the certificate, authenticating the certificate, and reading configuration parameters from the certificate are repeated upon a system reset/boot (figure 2: 130; column 6: lines 5-45; column 11: lines 26-30).

As per claim 4:

Geiger discloses a method, wherein the configuring step includes the step of configuring hardware in the processing device responsive to the configuration parameters (column 4: lines 23-35; column 10: lines 7-30).

As per claim 5:

Geiger discloses a method, wherein the configuring step includes the step of configuring software in the processing device responsive to the configuration parameters (column 10: lines 7-30).

As per claim 6:

Geiger discloses a processing device comprising:

processing circuitry (figure 1: 11);

a memory coupled to the processing circuitry (figure 4: 452);

wherein the processing circuitry:

accesses a certificate bound to the processing device and stored in the memory (column 3: lines 14-21; column 4: lines 23-35, lines 59-67; column 18: lines 45-60; column 19: lines 9-28);

authenticates the certificate (column 11: lines 46-54; column 16: lines 10-43;column 18: lines 1-5; column 18: lines 37-45);

reads configuration parameters from the certificate, if properly authenticated (column 4: lines 23-35);

configures the processing device responsive to the configuration parameters (column 4: lines 23-35).

As per claim 7:

Geiger discloses a processing device, wherein the processing circuitry accesses the certificate, authenticates the certificate, and reads configuration parameters from the certificate whenever the processing device is initially powered (figure 2: 130; column 6: lines 5-45).

As per claim 8:

Geiger discloses a processing device, wherein the processing circuitry accesses the certificate, authenticates the certificate, and reads configuration parameters from the certificate upon a system reset/boot (figure 2: 130; column 6: lines 5-45; column 11: lines 26-30).

As per claim 9:

Geiger discloses a processing device, wherein the processing circuitry configures hardware in the processing device responsive to the configuration parameters (column 4: lines 23-35; column 10: lines 7-30).

As per claim 10:

Geiger discloses a processing device, wherein the processing circuitry configures software in the processing device responsive to the configuration parameters (column 10: lines 7-30).

As per claim 11:

Geiger discloses a processing device, wherein the certificate can be created and modified only by the manufacturer of the processing device (column 17: lines 36-48).

As per claim 12:

Geiger discloses a method of configuring a processing device, comprising the steps of:

accessing a certificate bound to the processing device (column 3: lines 14-21; column 4: lines 23-35, lines 59-67; column 18: lines 45-60; column 19: lines 9-28);

authenticating the certificate (column 11: lines 46-54; column 16: lines 10-43;column 18: lines 1-5; column 18: lines 37-45);

reading configuration parameters from a data file associated with the certificate, if the certificate is properly authenticated (column 4: lines 23-35);

configuring the processing device responsive to the configuration parameters (column 4: lines 23-35.

As per claim 13:

Geiger discloses a method, wherein the steps of accessing the certificate, authenticating the certificate, and reading configuration parameters are performed whenever the processing device is initially powered (figure 2: 130; column 6: lines 5-45; column 11: lines 26-30).

As per claim 14:

Geiger discloses a method, wherein the steps of accessing the certificate, authenticating the certificate, and reading configuration parameters are repeated upon a system reset/boot (figure 2: 130; column 6: lines 5-45; column 11: lines 26-30).

As per claim 15:

Geiger discloses a method, wherein the configuring step includes the step of configuring hardware in the processing device responsive to the configuration parameters (column 4: lines 23-35; column 10: lines 7-30).

As per claim 16:

Geiger discloses a method, wherein the configuring step includes the step of configuring software in the processing device responsive to the configuration parameters (column 4: lines 23-35; column 10: lines 7-30).

As per claim 17:

Geiger discloses a processing device comprising:

processing circuitry (figure 1: 11);

a memory coupled to the processing circuitry (figure 4: 452);

wherein the processing circuitry:

accesses a certificate bound to the processing device and stored in the memory (column 3: lines 14-21; column 4: lines 23-35, lines 59-67; column 18: lines 45-60; column 19: lines 9-28);

authenticates the certificate (column 11: lines 46-54; column 16: lines 10-43;column 18: lines 1-5; column 18: lines 37-45);

reads configuration parameters from a data file associated with the certificate, if the certificate is properly authenticated (column 4: lines 23-35);

configures the processing device responsive to the configuration parameters (column 4: lines 23-35).

As per claim 18:

Geiger discloses a processing device, wherein the processing circuitry accesses the certificate, authenticates the certificate, and reads configuration parameters whenever the processing device is initially powered (figure 2: 130; column 6: lines 5-45).

As per claim 19:

Geiger discloses a processing device, wherein the processing circuitry accesses the certificate, authenticates the certificate, and reads configuration parameters upon a system reset/boot (figure 2: 130; column 6: lines 5-45; column 11: lines 26-30).

As per claim 20:

Geiger discloses a processing device, wherein the processing circuitry configures hardware in the processing device responsive to the configuration parameters (column 4: lines 23-35; column 10: lines 7-30).

As per claim 21:

Geiger discloses a processing device, wherein the processing circuitry configures software in the processing device responsive to the configuration parameters (column 4: lines 23-35; column 10: lines 7-30).

As per claim 22:

Geiger discloses a processing device, wherein the certificate can be created and modified only by the manufacturer of the processing device (column 17: lines 36-48).

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

See the notice of reference cited in form PTO-892 for additional prior art

Contact Information

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Techane J. Gergiso whose telephone number is (571) 272-3784 and fax number is **(571) 273-3784**. The examiner can normally be reached on 9:00am - 6:00pm. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Emmanuel Moise can be reached on (571) 272-3865. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

T. G

Techane Gergiso

Patent Examiner

Art Unit 2137

January 4, 2007

EMMANUEL L. MOISE
SUPERVISORY PATENT EXAMINER